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| Application Number:                  | 10643585   |  |  |  |
| International Application Number:    |  |  |  |  |
| Confirmation Number:                 | 4004   |  |  |  |
| Title of Invention:                  | LATENCY TOLERANT DISTRIBUTED SHARED MEMORY MULTIPROCESSOR COMPUTER |  |  |  |
| First Named Inventor/Applicant Name: | Steven L. Scott  |  |  |  |
| Customer Number:                     | 21186  |  |  |  |
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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

#### National Stage of an International Application under 35 U.S.C. 371

Fee Worksheet (PTO-06)

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### New International Application Filed with the USPTO as a Receiving Office

If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.